

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE COMMISSIONER OF PATENTS



HF
ZJW

Appl. No. : 10/757,586
Applicant : **Robert Edwards**
Filed : 01/15/04
Art Unit : 1753
Examiner : **Luan V. Van**
Docket No. : EI-2-04-001
Title : Method of Making Printed Circuit Board With Electroplated Conductive Thru Holes And Board Resulting Therefrom
Assignee : **Endicott Interconnect Technologies, Inc.**

Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

*Entered per RCE
filed 5/30/06
MA*

Dear Sir:

RESPONSE AFTER FINAL REJECTION

Responsive to the Office Action dated 03/28/06, made final by the Examiner, please amend the above-identified application as follows. Entry is urged to allow Applicants the opportunity to respond to the new grounds of rejections raised in the 03/28/06 Action. Should the Examiner persist in the rejection, entry is urged to place the claims in better condition for appeal. Applicants respectfully submit, however, that the Application is in condition for allowance in view of the provided amending and supporting argumentation, and that further searching and/or other work on the part of the Examiner is not needed.

There are no **Amendments to the Specification or Drawings** since none are needed.

Amendments to the Claims begin on page 2 of this Response.

Remarks/Arguments begin on page 6 of this Response.